

## **Blue Pearl Announces User Grey Cell™ Methodology with the Release of Version 7.1**

**Santa Clara**, California – September 27, 2013 -- [Blue Pearl Software](#), the provider of EDA software that accelerates FPGA verification, today announced that it is shipping Release 7.1 of its EDA software, Blue Pearl Software Suite, for Windows® and Linux® operating systems. It includes many clock domain crossing (CDC) enhancements including CDC waivers and User Grey Cell™.

“Release 7.1 addresses a critical verification bottleneck in the design flow,” said Ellis Smith, Chairman and CEO, Blue Pearl Software. “With the User Grey Cell™ methodology, designers using non-synthesizable and/or protected IP can specify design attributes that enables chip-level CDC.”

Quality IP and processors are critical to system-on-chip (SoC) designers worldwide due to the increasing number of systems using FPGAs in production or as prototypes. However, designers do not always have access to synthesizable RTL descriptions and thus resolve to black-boxing these modules. This results in verification blind spots that prevent the detection of critical metastability issues during the verification phase. The User Grey Cell™ methodology not only solves it but provides the end user with a flexible use model to catch chip-level CDC. Release 7.1 includes sample designs to illustrate the key concepts/features.

Release 7.1 new features include:

- User Grey Cell™
- CDC Waivers
- CDC checks more granular
- Black box signal direction detection
- FPGA-vendor specific options pages

### **Price and Availability**

Release 7.1 of the Blue Pearl Software Suite is available now. The base product, RTL analysis, starts at \$10K for a floating 1-YR TBL, with the other options FSM, CDC and Create priced at an additional \$5K, \$10K, and \$15K respectively. Please contact [sales@bluepearlsoftware.com](mailto:sales@bluepearlsoftware.com) to arrange a demo, or for additional pricing and upgrade information.

The Blue Pearl Software Suite is also available for online purchase via the Embedded Software Store or the Blue Pearl Software online store. For more information about the online stores, please visit <http://embeddedsoftwarestore.com/store> or <https://store.bluepearlsoftware.com/1112/catalog/catalog.1018/>.

For more information about Blue Pearl Software, please visit [www.bluepearlsoftware.com](http://www.bluepearlsoftware.com).

### **About Blue Pearl Software**

[Blue Pearl Software, Inc.](http://www.bluepearlsoftware.com) provides EDA software that accelerates FPGA design verification. The company's [Blue Pearl Software Suite](#) checks RTL designs for functional errors and automatically generates comprehensive and accurate Synopsys Design Constraints (SDC) to improve quality of results (QoR) and reduce FPGA design risks.

Visit Blue Pearl Software at <http://www.bluepearlsoftware.com>.

###

### **Press Contact:**

Shakeel Jeeawoody, Blue Pearl Software, +1- 408.961.0121, [shakeel@bluepearlsoftware.com](mailto:shakeel@bluepearlsoftware.com)