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Northwest Logic Uses Blue Pearl Software's Analyze to Maximize IP Core Quality

SAN JOSE, California—June 1, 2012 -[Blue Pearl Software, Inc.](#), a leading provider of EDA software which accelerates electronic design implementation, announced today that [Northwest Logic, Inc.](#), a leading provider of high-performance, easy-to-use, Intellectual Property (IP) cores, uses [Blue Pearl's Analyze](#) to maximize the quality of its IP cores.

“We have been using Blue Pearl's Analyze for several years and found it to be very useful in identifying and resolving coding issues. This, in turn, has helped us maximize the quality of our IP Cores. We not only use Analyze internally, but also recommend it to our customers and partners,” said Brian Daellenbach, President of Northwest Logic.

“Northwest Logic and Blue Pearl Software share the same focus on maximizing design quality. Analyze is one component of our Blue Pearl Software Suite which is specifically designed to increase our customers' Quality of Results (QoR),” added Shakeel Jeewoody, VP Marketing, Blue Pearl Software.“

About Blue Pearl Software Suite

Analyze is a part of [Blue Pearl Software Suite](#). It provides functional design analysis to verify properties, methodology standards and design rules. Its benefits include reducing design risk and improving QoR.

Blue Pearl Software Suite accelerates design implementation with its comprehensive RTL analysis, CDC checks and automatic SDC generation. Its Visualization Verification Environment™ and design technology give users immediate feedback for validating automatically generated pre-synthesis longest paths and SDCs, which are used to drive the efficiency of synthesis and place & route tools.

About Northwest Logic

[Northwest Logic](#), founded in 1995 and located in Beaverton, Oregon, provides high-performance, silicon-proven, easy-to-use IP cores including high-performance [Expresso Solution](#) (PCI Express 3.0,

2.1 and 1.1 cores and drivers), [Memory Interface Solution](#) (DDR3, DDR2, LPDDR2, Mobile DDR SDRAM; RLDRAM 3, RLDRAM II), and [MIPI Solution](#) (CSI-2, DSI). These solutions support a full range of platforms including ASICs, Structured ASICs and FPGAs. For additional information, visit <http://www.nwlogic.com> or contact info@nwlogic.com.

About Blue Pearl Software

[Blue Pearl Software, Inc.](#) provides next generation EDA software that uses new and innovative technology to reduce design flow iterations and increase designer productivity early in the digital design flow. [Blue Pearl Software Suite](#) checks RTL designs for functional errors and automatically generates comprehensive and accurate Synopsys Design Constraints (SDC) to improve quality of results (QoR) and reduce FPGA and ASIC design risks.

For more information, please visit Blue Pearl Software at <http://www.bluepearlsoftware.com>.

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Acronyms

ASIC: Application Specific Integrated Circuit
CDC: Clock Domain Crossing
EDA: Electronic Design Automation
FPGA: Field Programmable Gate Array
IP: Intellectual Property
QoR: Quality of Results
RTL: Register Transfer Level
SDC: Synopsys Design Constraints
SOC: System on Chip

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