Blue Pearl Software’s FPGA Tool Experts to Present at International Workshop on Logic and Synthesis

Who/What
Field-programmable gate array (FPGA) design tool experts from Blue Pearl Software, a leading provider of Electronic Design Automation (EDA) software, will be presenting at a Design Automation Conference related event at the Univ. of California, Berkeley.

At the International Workshop on Logic and Synthesis (IWLS), David E. Wallace, Senior Staff Engineer and Scott Aron Bloom, VP Research and Development will present on How Can We Build More Reliable EDA Software?

When/Where
Sunday, June 3, 2012
2:00pm - 2:30pm
Univ. of California, Berkeley

For More Information
International Workshop on Logic and Synthesis attendance requires pre-registration.
For more information, please visit http://www.iwls.org/iwls2012/.

To schedule a meeting with Blue Pearl, please click here.

About Blue Pearl Software
Blue Pearl Software, Inc. provides next generation EDA software that uses new and innovative technology to reduce design flow iterations and increase designer productivity early in the digital design flow. Blue Pearl Software Suite checks Register Transfer Level (RTL) designs for functional errors and automatically generates comprehensive and accurate Synopsys Design Constraints (SDC) to improve quality of results (QoR) and reduce FPGA and ASIC design risks.

Press Contact:
Georgia Marszalek, ValleyPR, LLC for Blue Pearl Software, +1 650 345 7477, Georgia@ValleyPR.com