

## **Verification Technology Inc. Transforms its FPGA Verification Flow by Standardizing on Blue Pearl Software**

**Santa Clara**, California – December 09, 2014 – Verification Technology Inc., a high-level verification service company that helps design teams reduce their total verification time, announced that it is using the Blue Pearl Software Suite as part of its FPGA verification methodology.

“Verification Technology Inc. is committed to providing the best verification flow to its customers,” said Yasumata Kubota, Chief Technology Officer, Verification Technology Inc. “Adding the Blue Pearl Software Suite to our flow allows us to include the latest generation EDA technologies to our advanced verification methodology.”

Verification Technology Inc. has completed over 350 verification SOC, ASIC or FPGA projects. By using the Blue Pearl Software Suite, our designers have greatly reduced the time spent in verification. Further development time decrease is enabled by early language construct checks, structural design checks and methodology specific checks. Other verification time reducing items include the use of the FSM analysis, clock domain crossing capability with User Grey Cell™ and automatic false path and multi-cycle path detection features of the Blue Pearl Software Suite.

“Blue Pearl Software is focused on automating FPGA and IP verification even before debugging starts,” said Ellis Smith, Chairman and CEO, Blue Pearl Software. “We are glad to help Verification Technology Inc. enhance their methodology in order to reduce their customer’s total verification time.”

### **Availability**

Release 8.3 of the Blue Pearl Software Suite is available now. Please contact [sales@bluepearlsoftware.com](mailto:sales@bluepearlsoftware.com) to arrange a demo, or for additional pricing and upgrade information.

For more information on how the Blue Pearl Software Suite is integrated in Verification Technology Inc's flow, please visit [http://vtech-inc.co.jp/profile/p04\\_e.html](http://vtech-inc.co.jp/profile/p04_e.html)

The Blue Pearl Software Suite is also available for online purchase via the Embedded Software Store or the Blue Pearl Software online store. For more information about the online stores, please visit <http://embeddedsoftwarestore.com/store> or <https://store.bluepearlsoftware.com/1112/catalog/catalog.1018/>.

### **About Blue Pearl Software**

[Blue Pearl Software, Inc.](#) provides EDA software that accelerates FPGA design verification. The company's [Blue Pearl Software Suite](#) checks RTL designs for functional errors and automatically generates comprehensive and accurate Synopsys Design Constraints (SDC) to improve quality of results (QoR) and reduce FPGA design risks.

Visit Blue Pearl Software at <http://www.bluepearlsoftware.com>.

### **About Verification Technology Inc.**

Verification Technology Inc. is creating an excellent verification environment that will enable customers to readily achievable short TAT verification. More specifically, Verification Technology Inc. is concentrated on creating the verification environment for standard peripheral, such as USB3.0, PCI Express and Ethernet. Our goal is to provide excellent verification service with high level technique. Visit Verification Technology Inc. at <http://vtech-inc.co.jp>

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